

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6,839,806").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 16:27
S1	1	(controller same connection same ram same buffer).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:24
S2	50	(controller same buffer same tag).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:26
S3	1	(controller same "second buffer" same tag).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:26
S4	9	(controller same buffers same tag).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:35
S5	0	(controller and "tag buffer" and ram).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:36
S6	21	(controller and buffers and ram).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:36
S7	1	controller same "tag buffer" same ram same bus	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:37
S8	6	controller same "tag buffer" same ram	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:37
S9	2	controller same "buffer interface" same tag\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:38
S10	8	controller same "tag buffer" same ((random adj access) ram)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:39
S11	67	controller same "second buffer" same ((random adj access) ram)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:40
S12	1	controller same "second buffer" same ((random adj access) ram) and "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:48
S13	1	("5226144").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:42
S14	1	("5829040").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:47

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S15	1	("6651143").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:48
S16	7	controller same buffers same "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 15:51
S17	60	controller same buffers same tag same bus same interface	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/27 16:15
S18	1	controller same buffers same (tag adj buffer) same interface	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:01
S19	332	controller same buffer same "system memory" same access same control	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:01
S20	4	controller same buffer same "system memory" same access same control and "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:03
S21	1	controller same buffers same "system memory" same access and "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:13
S22	10	controller same buffers same memory same access and "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:14
S23	193	controller same buffers same memory same access and (tag\$1 same buffer)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:17
S24	3	controller same buffers same memory and (tag\$1 same buffer) and "buffer interface".ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:17
S25	62	controller same buffers same memory and (tag\$1 same buffer) and (buffer and interface).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:24
S26	4	controller same buffers same memory and (tag\$1 same buffer) and (buffer\$1 adj interface\$1).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:24
S27	5	controller same buffer\$1 same memory and (tag\$1 near2 buffer) and (buffer\$1 adj interface\$1).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:25
S28	75	controller same buffer\$1 same memory same bus same interface and (tag\$1 near2 buffer)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:28
S29	9	"buffer interface".ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:29

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S30	74	"buffer interface".ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:37
S31	29	"buffer interface".ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:49
S32	805	"buffer interface" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:49
S33	7	"buffer interface" same controller same bus same control same access and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:51
S35	4	buffer\$1 same interface same controller same bus same access same memory and "tag buffer" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 12:55
S36	1	buffer\$1 same controller same (bidirectional bi) adj2 bus same memory and "tag buffer" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 13:27
S37	2	buffer\$1 same controller same bus same memory and "tag buffer" and 7\$2/\$3.ccls. and subsystem.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 13:28
S38	11	buffer\$1 same controller same bus same memory and "tag buffer" and 7\$2/\$3.ccls. and (interface subsystem buffer\$1).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:02
S39	7	tag adj buffer\$1 same controller same bus same memory and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:06
S40	2	tag adj buffer\$1 same controller same bus same memory same buffer near4 (read\$1 and write\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:07
S41	16	controller same bus same memory same buffer near4 (read\$1 and write\$1) and (tag near2 buffer\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:08
S42	29	controller same bus same memory same buffer\$1 same (read\$1 near2 write\$1) and (tag near2 buffer\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:09
S43	53	controller same memory same buffer\$1 same (read\$1 near2 write\$1) and (tag near2 buffer\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:49
S44	11	controller same memory same buffer\$1 same (read\$1 near2 write\$1) and (tag adj buffer\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:50

## EAST Search History

S45	16	controller same memory same (bus\$2 (data adj line\$1)) and buffer\$1 same (read\$1 adj queue\$1) same (write\$1 adj queue\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:53
S46	8	controller same memory same buffer\$1 same (read\$1 adj queue\$1) same (write\$1 adj queue\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 15:59
S47	68	controller same memory same buffer\$1 same read\$3 same writ\$3 and ((read\$1 adj queue\$1) same (write\$1 adj queue\$1))	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 16:01
S48	1	controller same memory same buffer\$1 same read\$3 same writ\$3 and ((read\$1 adj queue\$1) same (write\$1 adj queue\$1)) and tag adj buffer\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 16:00
S49	1	controller same memory same buffer\$1 same read\$3 same writ\$3 and ((read\$1 adj queue\$1) same (write\$1 adj queue\$1)) and "second buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 16:00
S50	21	controller same memory same buffer\$1 same read\$3 same writ\$3 and ("read queue" same "write queue") and tag\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 16:04
S51	194	"second buffer" and buffer\$1 near3 tag\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 16:05
S52	56	"second buffer" and buffer\$1 near3 tag\$1 and 7\$2/\$3.ccls. and buffer.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 16:07
S53	12	"second buffer" near3 tag\$1 and 7\$2/\$3.ccls. and buffer.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 16:24
S54	67	buffer near3 "tag buffer" and 7\$2/\$3.ccls. and buffer.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/28 17:04
S55	6	(designat\$3 set\$4) near5 "tag buffer" and 7\$2/\$3.ccls. and buffer.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 11:58
S56	1	designat\$3 near6 "tag buffer" and 7\$2/\$3.ccls. and buffer.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 11:59
S57	5	(designat\$3 appoint\$3 set\$4) near3 "tag buffer" and 7\$2/\$3.ccls. and buffer.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 12:01

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S58	1	(designat\$3 appoint\$3 made) near3 "tag buffer" and 7\$2/\$3.ccls. and buffer.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 12:02
S59	4	"data buffer" same "tag buffer" same controller	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 12:08
S60	10	"data buffer" same "tag buffer" and controller same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 12:11
S61	21	"data buffer" same "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:28
S62	30	designat\$3 near5 (tag near3 buffer)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:29
S63	1	(tag\$1 near3 buffer) same "tag control input"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:30
S64	1	buffer same "tag control input"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:30
S65	97	buffer same "tag control"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:31
S66	1	buffer near5 ("tag control" near2 signal)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:31
S67	6	buffer same ("tag control" near2 signal)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:31
S68	52	buffer same "tag control" and buffer\$1.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:31
S69	39	buffer same "tag control" and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:38
S70	0	buffer same controller same memory same bus same interface same read same write same logic\$2 same decode and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:39
S71	0	buffer same controller same memory same bus same interface same read same write and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:39

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S73	8	buffer\$1 same controller same memory same bidirectional adj bus same read\$3 same writ\$3 and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:55
S74	71	buffer\$1 same controller same memory same random adj access same bus same read\$3 same writ\$3 and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 13:58
S75	1	buffer\$1 same controller same memory same random adj access same bus same read\$3 same writ\$3 same queue\$1 and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:00
S76	1	buffer\$1 same controller same memory same bus same (read\$3 near2 queue\$1) same (writ\$3 near2 queue\$1) and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:04
S77	42	buffer\$1 same controller same memory same bus and (read\$3 near2 queue\$1) same (writ\$3 near2 queue\$1) and buffer\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:06
S78	51	buffer\$1 same controller same memory same bus and ("read queue" same "write queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:11
S79	14	buffer\$1 same controller same memory same bus same (RAM random adj access) and ("read queue" same "write queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:13
S80	75	buffer\$1 same controller same memory and ("read queue" same "write queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:17
S81	5	buffer\$1 same "memory controller" same decod\$3 same bus and ("read queue" same "write queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:18
S82	0	buffer\$1 same "memory controller" same bus same (RAM random adj access) and ("read queue" same "write queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:18
S83	1	buffer\$1 same "memory controller" same bus same (RAM random adj access) and (read and write) near3 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:19
S84	95	buffer\$1 same "memory controller" same bus and (read and write) near3 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:21

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S85	112	buffer\$1 same "memory controller" same bus and (read\$1 and write\$1) near3 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:24
S86	9	buffer\$1 same "memory controller" same bus and (read\$1 adj queue) same (write\$1 adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:26
S87	67	buffer\$1 same controller same memory same bus and (read\$1 adj queue) same (write\$1 adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:28
S88	14	buffer\$1 same controller same memory and (RAM same bus) and (read\$1 adj queue) same (write\$1 adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:28
S89	27	buffer\$1 same "memory controller" and (read\$1 adj queue) same (write\$1 adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:29
S90	30	buffer\$1 same memory near2 controller and (read\$1 adj queue) same (write\$1 adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:35
S91	15	buffer\$1 same memory near2 controller same bus and (read\$3 adj queue) same (write\$3 adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:37
S92	354	buffer\$1 same controller same bussame read same write and (read adj queue) and (write adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:38
S93	42	buffer\$1 same controller same bus same read same write and (read adj queue) and (write adj queue)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:38
S95	67	buffer\$1 same controller same memory same read\$1 same write\$1 and ("read queue") and ("write queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:41
S96	130	buffer\$1 same controller same memory same read\$1 same write\$1 same between same interface same decod\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:55
S97	24	buffer\$1 same controller same memory same read\$1 same write\$1 same between same interface same decod\$3 same (RAM random adj access) same bus	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:43
S98	88	buffer\$1 same controller same memory same read\$1 same write\$1 same between same interface same decod\$3 same bus	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:43
S99	0	"tag bus" same "tag buffer" same "tag control"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:56

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S10 0	7	"tag bus" same "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:56
S10 1	1	"tag bus" and "tag buffer" same (another second different) adj buffer	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:57
S10 2	5	"tag bus" and "tag buffer" and tag near3 signal	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:58
S10 3	12	"tag bus" and "tag buffer" same signal	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:59
S10 4	24	tag\$1 near2 bus and "tag buffer" same signal\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:59
S10 5	102	tag\$1 near2 bus and tag\$1 near2 buffer same signal\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 14:59
S10 6	77	"tag bus" and tag\$1 near2 buffer same signal\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:02
S10 7	40	"tag bus" and tag\$1 adj2 buffer same signal\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:03
S10 8	24	tag near2 bus and "tag buffer" same signal\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:04
S10 9	12	"tag buffer" near3 signal\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:06
S11 0	1	designat\$3 near3 "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:06
S11 1	4	(designating setting) near3 "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:07
S11 2	16	(designat\$3 indicat\$3) near3 "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:07
S11 3	5	(designat\$3 indicat\$3) adj4 "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:10
S11 4	9	tag adj3 signal same "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:13



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S11 5	287	buffer same "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:13
S11 6	287	buffer near3 "tag buffer"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:14
S11 7	207	buffer near3 "tag buffer" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:16
S11 8	8	tag near2 input same "tag buffer" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:21
S11 9	1	"tag buffer" same controller same memory same decod\$3 same interface same read\$1 same write\$1 same bus	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:22
S12 0	1	"tag buffer" same controller same memory same decod\$3 same interface same read\$1 same write\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:23
S12 1	7	"tag buffer" same controller same memory same decod\$3 same read\$1 same write\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:23
S12 2	6	buffer near3 tag\$1 same "control input"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:25
S12 3	116	buffer near3 tag\$1 same designat\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:32
S12 4	47	designat\$3 adj5 ((buffer storage register) near3 tag\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:33
S12 5	24	designat\$3 adj5 ((buffer storage) near3 tag\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:33
S12 6	73	(designat\$3 assign\$3 setting appoint\$3) adj5 ((buffer storage) near3 tag\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:34
S12 7	235	designat\$3 same ((buffer storage) near3 tag\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:35
S12 8	60	designat\$3 same ((buffer storage) near3 tag\$1) and tag\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:39
S12 9	2	designat\$3 near3 (tag adj (buffer storage)) and tag\$1.ab. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:40

## EAST Search History

S13 0	40	designat\$3 same (tag adj (buffer storage)) and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:42
S13 1	0	becom\$3 adj2 "tag buffer" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:43
S13 2	4	(designat\$3 becom\$3 mak\$3) near3 "tag buffer" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:45
S13 3	3	(designat\$3 becom\$3 mak\$3 sets setting) adj3 "tag buffer" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 15:58
S13 4	8	(designat\$3 becom\$3 mak\$3 sets setting) adj3 ("tag buffer" "tag storage") and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 16:24
S13 5	3	"tag buffer" same interface same bus same memory same read same write	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 16:38
S13 6	3	"tag buffer" same interface same bus same memory same decod\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 16:38
S13 7	3	"tag buffer" same interface same memory same decod\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:22
S13 8	23	read adj2 queue and write adj2 queue and tag adj2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:32
S13 9	25	read\$1 adj2 queue and write\$1 adj2 queue and tag\$1 adj2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:39
S14 0	80	read\$1 near2 queue and write\$1 near2 queue and tag\$1 near2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:41
S14 1	3	"read data queue" and "write data queue" and tag\$1 near2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:42
S14 2	16	"read queue" and "write queue" and tag\$1 near2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:44
S14 3	1	"read queue" and "write queue" and ("tag queue" "tag data queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:44
S14 4	19	read adj2 queue and write adj2 queue and ("tag queue" "tag data queue")	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/29 17:47

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S14 5	84	buffer same read adj2 queue same write adj2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 11:06
S14 6	200	decod\$3 near3 buffer same control same commands	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 11:07
S14 7	394	decod\$3 near3 buffer near3 (instructions commands)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 12:48
S14 8	148	interleav\$3 near3 (read and write) same operations	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 13:03
S14 9	22	interleav\$3 near3 (read and write) same operations same buffer	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 13:03
S15 0	16	interleav\$3 near3 (read and write) near3 operations same buffer	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 14:43
S15 1	1	"read queue" and "write queue" and "tag queue"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 14:43
S15 2	5	"read queue" and "write queue" and tag adj2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 14:44
S15 3	23	read adj2 queue and write adj2 queue and tag adj2 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 14:45
S15 4	39	read adj3 queue and write adj3 queue and tag adj3 queue	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 16:27



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